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				Application Number	Not Yet Assigned
				Filing Date	25 JUL 2003
				First Named Inventor	Leonard Forbes
				Art Unit	2816
				Examiner Name	Q. Tra
Sheet	1	of	1	Attorney Docket Number	M4065.0181/P181-B

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
Q	AA	*4,860,322	08/1989	Lloyd	375 / 356
	AB	*5,307,381	04/1994	Ahuja	395 / 356
	AC	*5,398,262	03/1995	Ahuja	395 / 356
	AD	*5,546,023	08/1996	Borkar et al.	326 / 93
	AE	*5,586,307	12/1996	Wong et al.	713 / 400
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	AG	*5,812,708	09/1998	Rao	385 / 14
	AH	*6,307,405	10/2001	Forbes et al.	327 / 54

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

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<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See attached Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the application number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
Q	CA	*Rabaey; "DIGITAL INTEGRATED CIRCUITS" Prentice Hall Electronics and VLSI Series		
Q	CB	*Maliniak; "DAC attacks designer issues. (Design Automation Conference)(includes related articles)"; 1995; 13 total pages		
Q	CC	*Kim et al.; "Characteristics of Integrated Dipole Antennas on Bulk, SOI, and SOS Substrates for Wireless Communications", IEEE; 1998; pgs 98-21, 98-23.		
Q	CD	*Deutsch et al.; "When are Transmission-Line Effects Important for On-Chip Interconnections?", IEEE; 1997; pgs 1836-1845.		
Q	CE	*Blalock et al., "A High-Speed Sensing Scheme for 1T Dynamic RAM's Utilizing the Clamped Bit-Line Sense Amplifier," IEEE Journal of Solid-State Circuits, 27(4), 618-625, (Apr. 1992).		

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